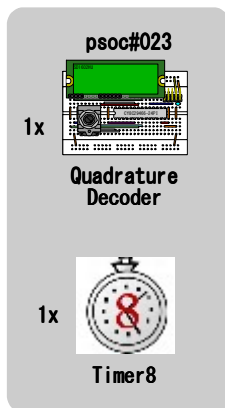




15+

024

Quadrature Decoder
by TimerInterrupt



Timer8_1INT.asm

```
_Timer8_1_ISR:  
    ljmp _timer8_1_int_func  
    reti
```

main.c

```
#define RA 0x80          // RA = P0[7]  
#define RB 0x20        // RB = P0[5]  
BYTE csw,psw=0xFF,count=0;  
  
#pragma interrupt_handler timer8_1_int_func  
void timer8_1_int_func(void)  
{  
    csw = PRT0DR; // Current Switch  
    if ((~psw&RA)&&(csw&RA)&&(csw&RB)) count--;  
    if ((~psw&RB)&&(csw&RB)&&(csw&RA)) count++;  
    psw = csw;  
}  
  
void main()  
{  
    PRT0DM2&=~0xA0; PRT0DM1|=0xA0; PRT0DM0|=0xA0;  
    PRT0DR = RA | RB // RA,RB PullUp  
    LCD_1_Start();  
    Timer8_1_Start();  
    Timer8_1_EnableInt();  
    M8C_EnableGInt;  
  
    LCD_1_PrCString("*** PSoC024 ***");  
    while(1) {  
        LCD_1_Position(1,0);  
        LCD_1_PrHexByte(count);  
    }  
}
```

Properties - Timer8_1	
Name	Timer8_1
User Module	Timer8
Version	2.6
Clock	CPU_32_KHz
Capture	Low
TerminalCountOut	None
CompareOut	None
Period	255
CompareValue	127
CompareType	Less Than Or Equal
InterruptType	Compare True
ClockSync	Sync to SysClk
TC_PulseWidth	Full Clock

