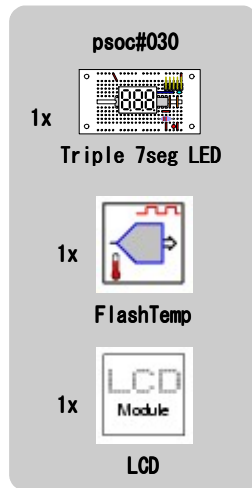




15+

031

FlashTemp Test



Global Resources - psoc031

Power Setting [ V 5.0V / 24MHz	
CPU_Clock	SysClk/8
32K_Select	Internal
PLL_Mode	Disable
Sleep_Timer	512_Hz
VC1= SysClk/N	16
VC2= VC1/N	16
VC3 Source	SysClk/1
VC3 Divider	1
SysClk Source	Internal
SysClk*2 Disable	No
Analog Power	SC On/Ref Low
Ref Mux	(Vdd/2)+/-BandG
Op-Amp Bias	Low
A_Buff_Power	Low

CPU\_Clock

Properties - FlashT...

Name	FlashTemp_1
User Module	FlashTemp
Version	2.2

Properties - LCD\_1

Name	LCD_1
User Module	LCD
Version	1.5
LCDPort	None
BarGraph	Enable

LCDPort

main.c

```
float cTemp;
BYTE aTemp,oTemp=12; // offset adjustment
void main() {
    PRT2DM2=0x00; PRT2DM1=0xEB; PRT2DM0=0xFF;
    PRT0DM2=0xF8; PRT0DM1=0xFB; PRT0DM0=0x07;
    M8C_EnableGInt;
    FlashTemp_1_Start();
    for (;;) {
        if (FlashTemp_1_fIsData()) {
            cTemp = cTemp-cTemp/100+
(float)(FlashTemp_1_cGetData()+oTemp)/100;
            SegA[2] = (cTemp<0)?0x0002:0x0000;
            SegS[2] = 0xF814;
            aTemp = abs(cTemp);
            FlashTemp_1_Start();
        }
        Disp7seg((aTemp/10<<4) + (aTemp%10));
        LCD_1_Delay50uTimes(100);
    }
}
```

